

## INITIAL INFORMATION DATA SHEET

### APPLICATION INFORMATION

Application Type:: Regular  
Subject Matter:: Utility  
Title:: INTEGRATED TESTING OF  
SERIALIZER/DESERIALIZER IN FPGA  
Docket Number:: X-895 US  
Request for Early Pub?:: No  
Request for Non-Pub?:: Yes  
Total Drawing Sheets:: 3  
Small Entity?:: No

### INVENTOR INFORMATION

Inventor Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Given Name:: Austin H.  
Family Name:: Lessea  
Street:: 25542 Mt. Bache Road  
City:: Los Gatos  
State or Province:: CA  
Postal or Zip Code:: 95033

### CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

**REPRESENTATIVE INFORMATION**

<b>Representative Customer Number::</b>	<b>24309</b>	
<b>Representative Designation::</b>	<b>Registration Number::</b>	<b>Representative Name::</b>
Associate	39,838	Russell D. Slifer

**ASSIGNEE INFORMATION**

Assignee Name:: Xilinx, Inc.  
Street:: 2100 Logic Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95124